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1446 TO

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Application No.	: 09/942,979
Filing Date	: August 30, 2001
First Named Inventor:	N. MATSUOKA
Group Art Unit	: 2664
Examiner Name	: W. CHIN
Attorney Docket No.	: FUJG 18.949

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document	Kind Code if known ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
SC		5,465,348		AMEMIYA, et al.	11/07/1995	

FOREIGN DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document Office ³ Number ⁴ Kind Code ⁵ (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YY/Y	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
		0 569 172	EP	AMERICAN TELEPHONE AND TELEGRAPH COMPANY	11/10/1993	

Other Prior Art-Non Patent Literature Documents

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check here if English language translation attached
		WONG, et al., "A PROGRAMMABLE RATE-BASED SCHEDULER (PRS) FOR ATM SWITCHES AND MULTIPLEXERS" GLOBAL TELECOMMUNICATIONS CONFERENCE, 1997. NOVEMBER 1997; XP010254750, ISBN: 0-7803-4198-8; pp. 827-832	RECEIVED FEB 23 2004 Technology Center 2600
		ERMEDAH, et al., "RESPONSE-TIME GUARANTEES IN ATM NETWORKS" REAL-TIME SYSTEMS SYMPOSIUM, DECEMBER 1997 XP010260397; ISBN: 0-8186-8268-X, pp. 274-284	
		BYUNG KOOK KIM, et al., "SCALABLE HARDWARE EARLIEST-DEADLINE-FIRST SCHEDULER FOR ATM SWITCHING NETWORKS" REAL-TIME SYSTEMS SYMPOSIUM, DECEMBER 1997 XP010260391, ISBN: 0-8186-8268-X, pp. 210-218	
Examiner Signature		<i>for [signature]</i>	Date Considered 4/11/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet 1 of 1

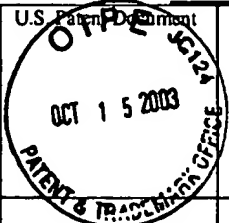
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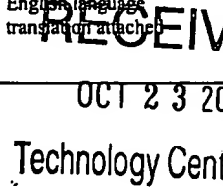

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		00/64109	WO	CISCO TECHNOLOGY, INC.	10/26/2000	

Other Prior Art-Non Patent Literature Documents

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check her if English language translation attached
		KIRSTADTER, "FAIR AND FLEXIBLE CONTENTION RESOLUTION FOR INPUT BUFFERED ATM SWITCHES BASED ON LAN MEDIUM ACCESS CONTROL PROTOCOLS" BROADBAND COMMUNICATIONS: GLOBAL INFRASTRUCTURE FOR THE INFORMATION AGE. PROCEEDINGS OF THE INTERNATIONAL IFIP-IEEE CONFERENCE ON BROADBAND COMMUNICATIONS APRIL 23, 1996, XP010525736; ISBN: 0-412-75970-5; pp. 370-381	
		SERPANOS, et al., "HIGH-SPEED CELL SCHEDULING FOR ROUTER BACKPLANES" PROCEEDINGS OF THE IEEE CONFERENCE 2000 ON HIGH PERFORMANCE SWITCHING AND ROUTING. JUNE 26-29, 2000; XP001075687; ISBN: 0-7803-5884-8, pp. 65-71	
		DUAN, et al., "A HIGH-PERFORMANCE OC-12/OC-48 QUEUE DESIGN PROTOTYPE FOR INPUT-BUFFERED ATM SWITCHES" INFOCOM 97. SIXTEENTH ANNUAL JOINT CONFERENCE OF THE IEEE COMPUTER AND COMMUNICATIONS SOCIETIES. DRIVING THE INFORMATION REVOLUTION., PROCEEDINGS IEEE APRIL 7-11, 1997; XP010252017; ISBN: 0-8186-7780-5, pp. 20-28	
Examiner Signature		Date Considered	4/10/05

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